Reduced Instruction Set Computer (RISC)

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Reduced Instruction Set Computer (RISC) is a computer that uses a Central Processing Unit (CPU) that implements the processor design principle of simplified instructions. To date, RISC is the most efficient CPU architecture technology.

This architecture is an evolution and alternative to Complex Instruction Set Computer (CISC). With RISC, the basic concept is to have simple instructions that do less but execute very quickly to provide better performance.

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